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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:
D'ALBORE ET AL.

Serial No. 10/820,462

Filing Date: April 8, 2004

For: METHOD FOR PATCHING ROM
INSTRUCTIONS IN AN ELECTRONIC
EMBEDED SYSTEM INCLUDING AT
LEAST A FURTHER MEMORY PORTION)

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)
)
) Attorney Docket:
) 53801

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop: PGPUB-Drawings
Commissioner for Patents
Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed are three (3) sheets of formal drawings to
be filed in the above-identified patent application.

Respectfully submitted,

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